

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Martin KNAIPP et al.

Serial No.: 10/591,001

Filed: June 25, 2007

For: High-Voltage PMOS Transistor

Examiner: LEE, Eugene  
Group Art: 2815

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

SIR:

This is in response to the Restriction Requirement dated December 3, 2008 setting a shortened statutory period for response expiring January 3, 2009.

Applicants hereby elect claims 1-6 of Group I drawn to a high voltage PMOS transistor for continued prosecution.

This election is made without traverse.

Applicants reserve the right to pursue the non-elected claims in a divisional application prior to issuance of a patent on the instant application.

Substantive examination of the present application is respectfully solicited.

Respectfully submitted,  
COHEN PONTANI LIEBERMAN & PAVANE LLP

By /Thomas Langer/  
Thomas Langer  
Reg. No. 27,264  
551 Fifth Avenue, Suite 1210  
New York, New York 10176  
(212) 687-2770

Dated: December 30, 2008